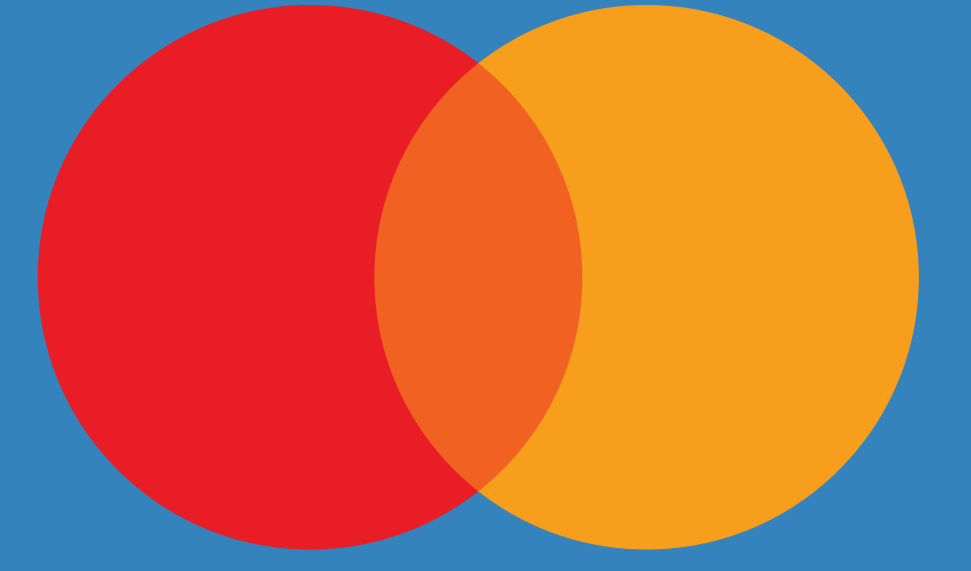


**NANYANG
TECHNOLOGICAL
UNIVERSITY**
SINGAPORE

College of Computing
and Data Science



Hardware Security and Design for Trust

This course provides an introduction to hardware security risks and design-level countermeasures to ensure hardware trust.

Securing hardware against potential attacks is crucial for the safety and resilience of modern technology. This course empowers learners to address critical security challenges with design-level solutions, fostering innovation and trust in the development of robust and reliable hardware systems.



SSG Funding support is available for this course:

- Fees before SSG funding and GST is S\$2,000.00
- Singapore Citizens (SCs) and Permanent Residents, (Up to 70% funding)
- SCs aged ≥ 40 years old will enjoy the SkillsFuture Mid-career Enhanced Subsidy, (Up to 90% funding)

[Register](#)

Gwee Bah Hwee:

Assistant Chair (Outreach), EEE, NTU

He is the Deputy Director of Integrated Centre for Evaluation (NiCE). He has published more than 150 technical papers, and 5 granted US patents.

Cheng Deruo:

Research Scientist with Temasek Laboratories, NTU

His research focus on AI-based inspection and security analysis of hardware systems and electronics.

Class Schedule:

16 November (Sat) – 23 November (Sat)

Live online sessions on two Saturdays (9:30 am - 11:30 am)

Live E-consultation on Wednesday (7:30pm – 9.00 pm)

Additional online learning resources provided for self-paced learning

